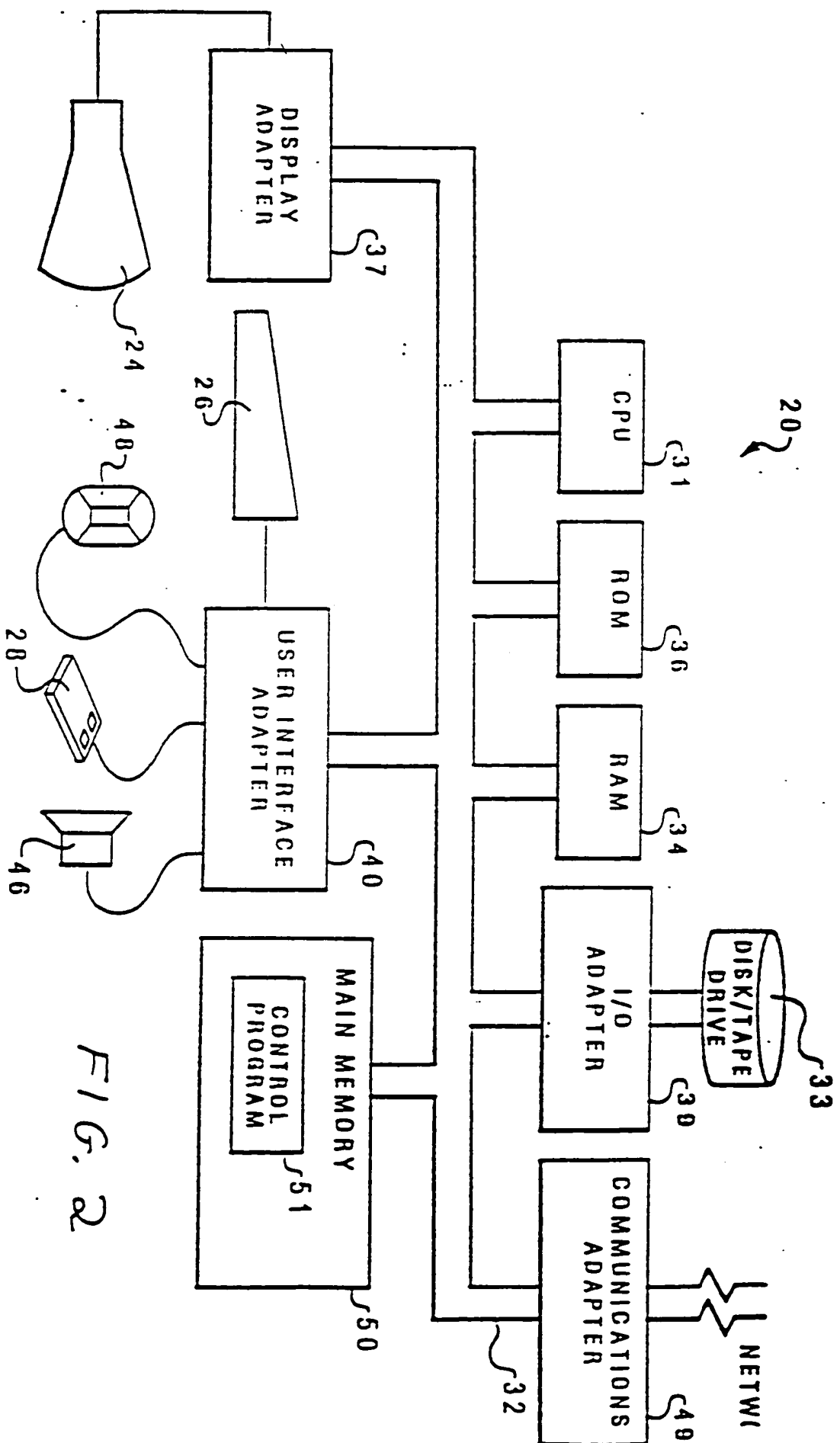


FIG. 1



F1G.2

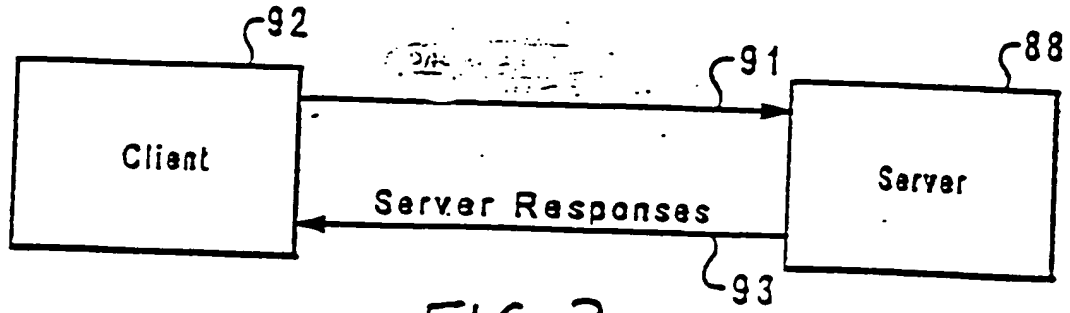


FIG. 3

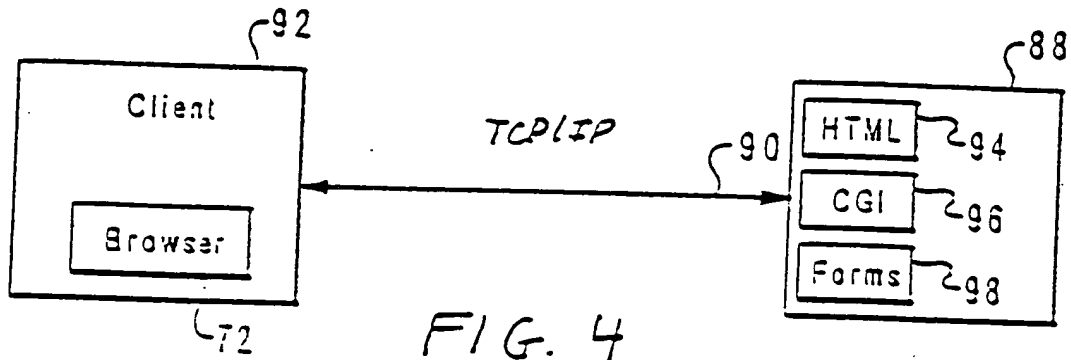


FIG. 4

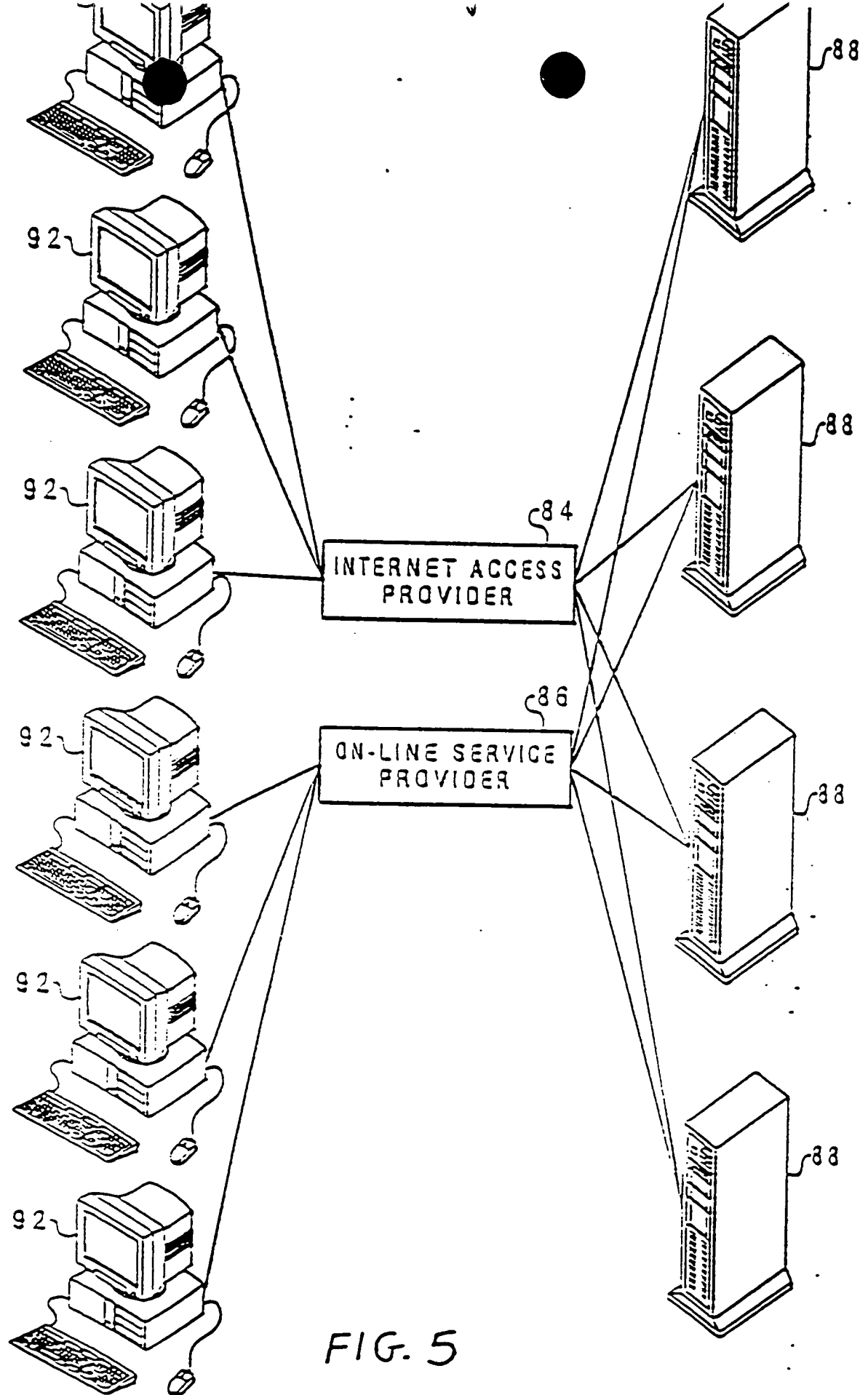
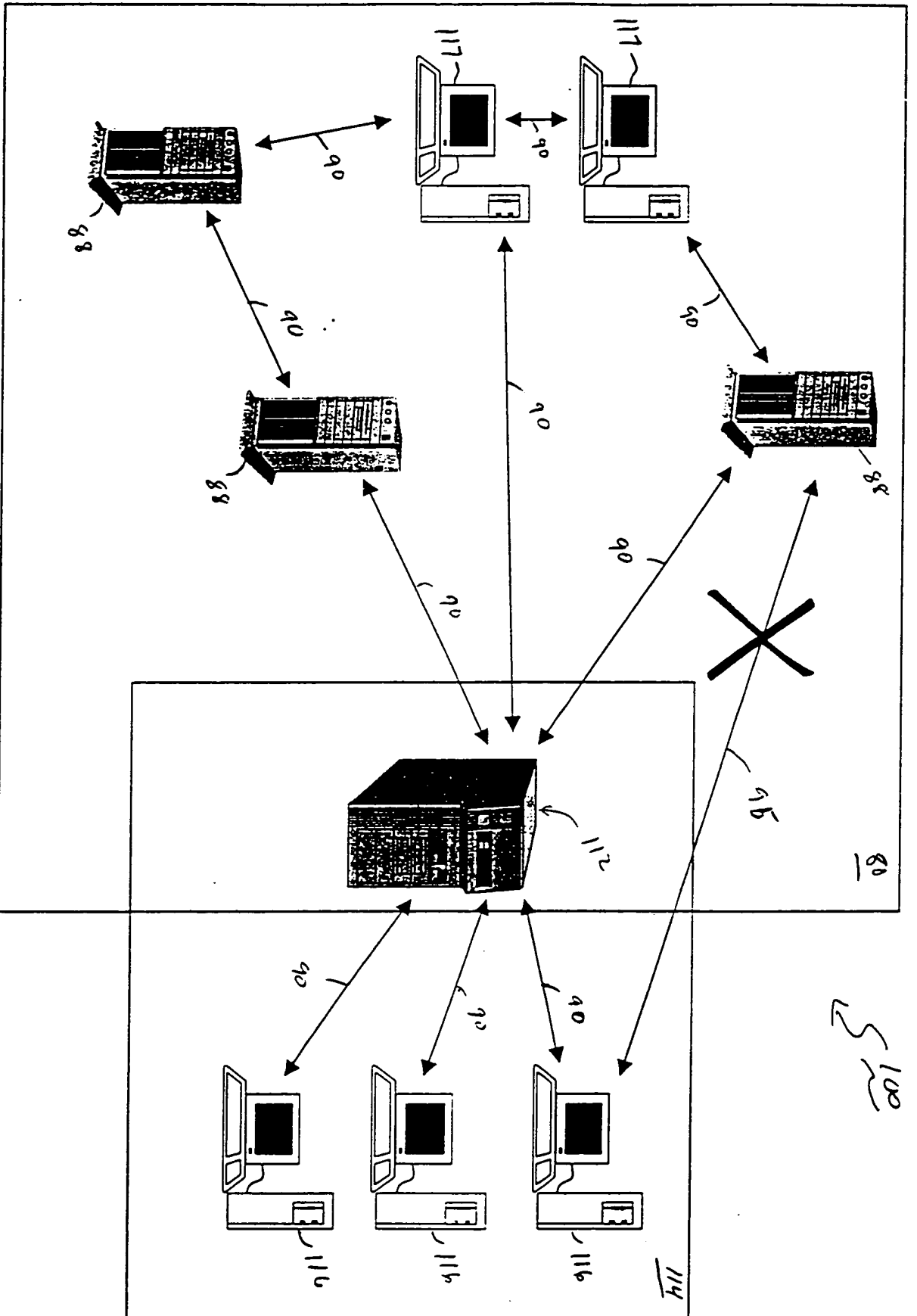


FIG. 5

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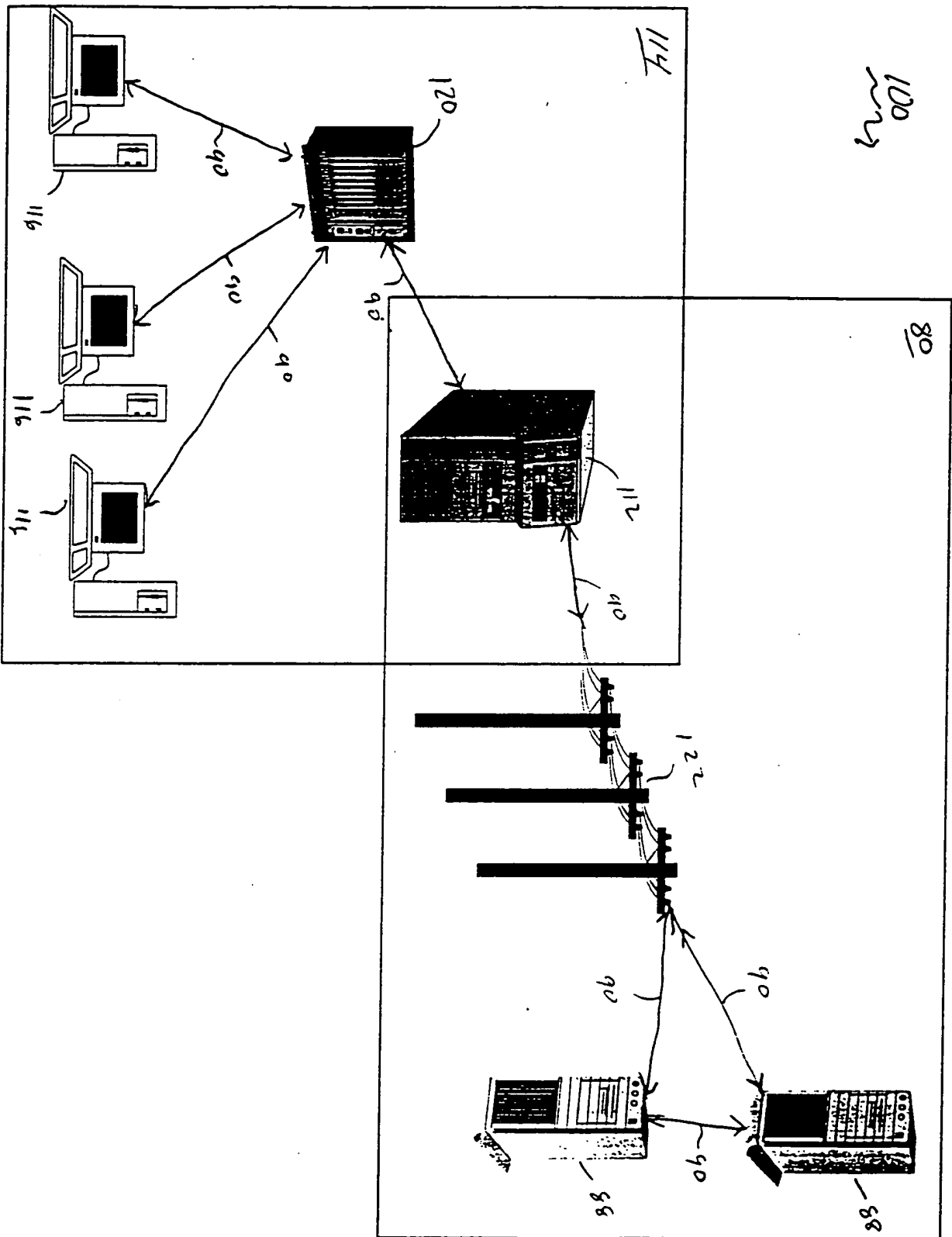


Fig 7

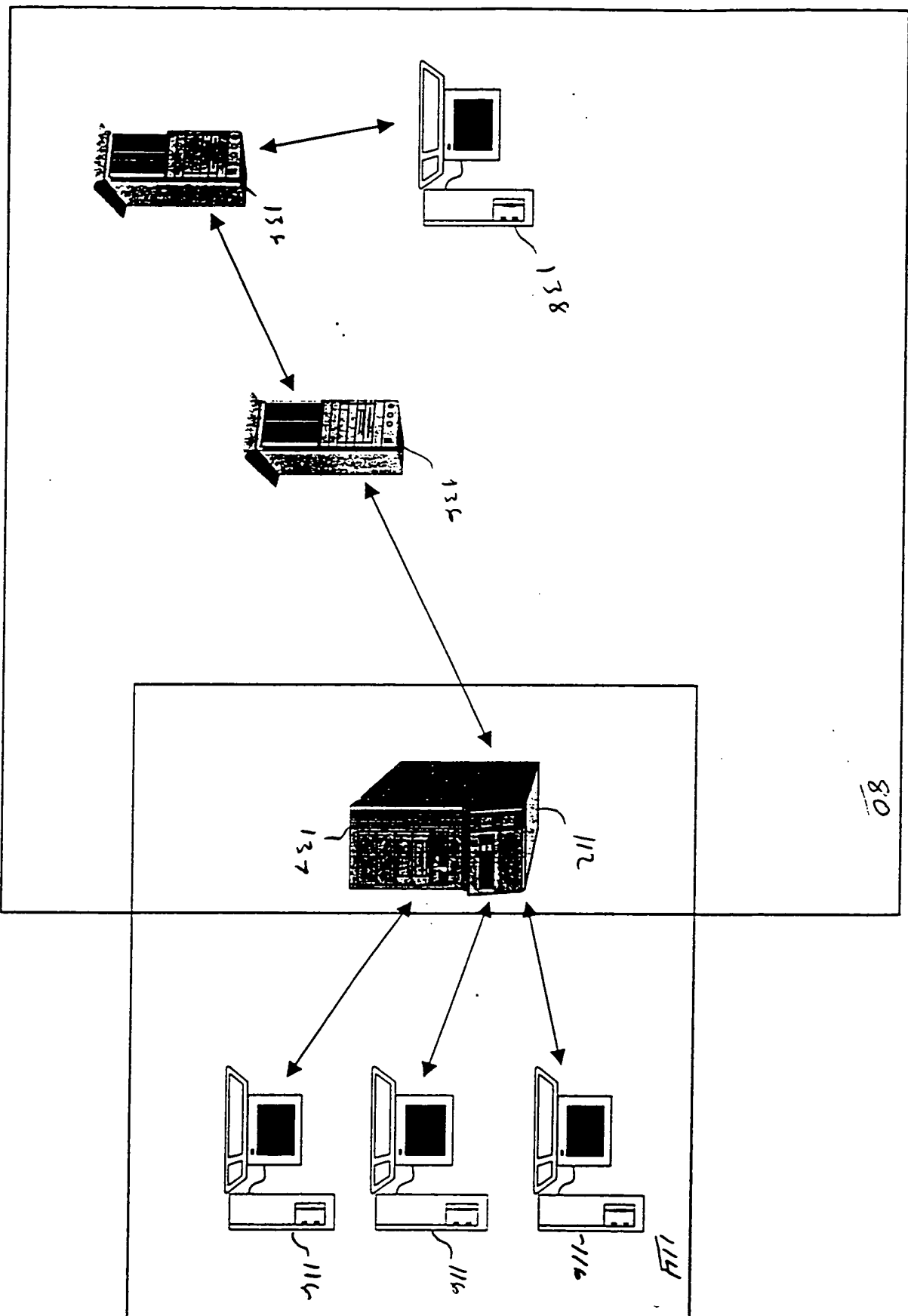


Fig 10

FIG. 10 is a block diagram of a system architecture.

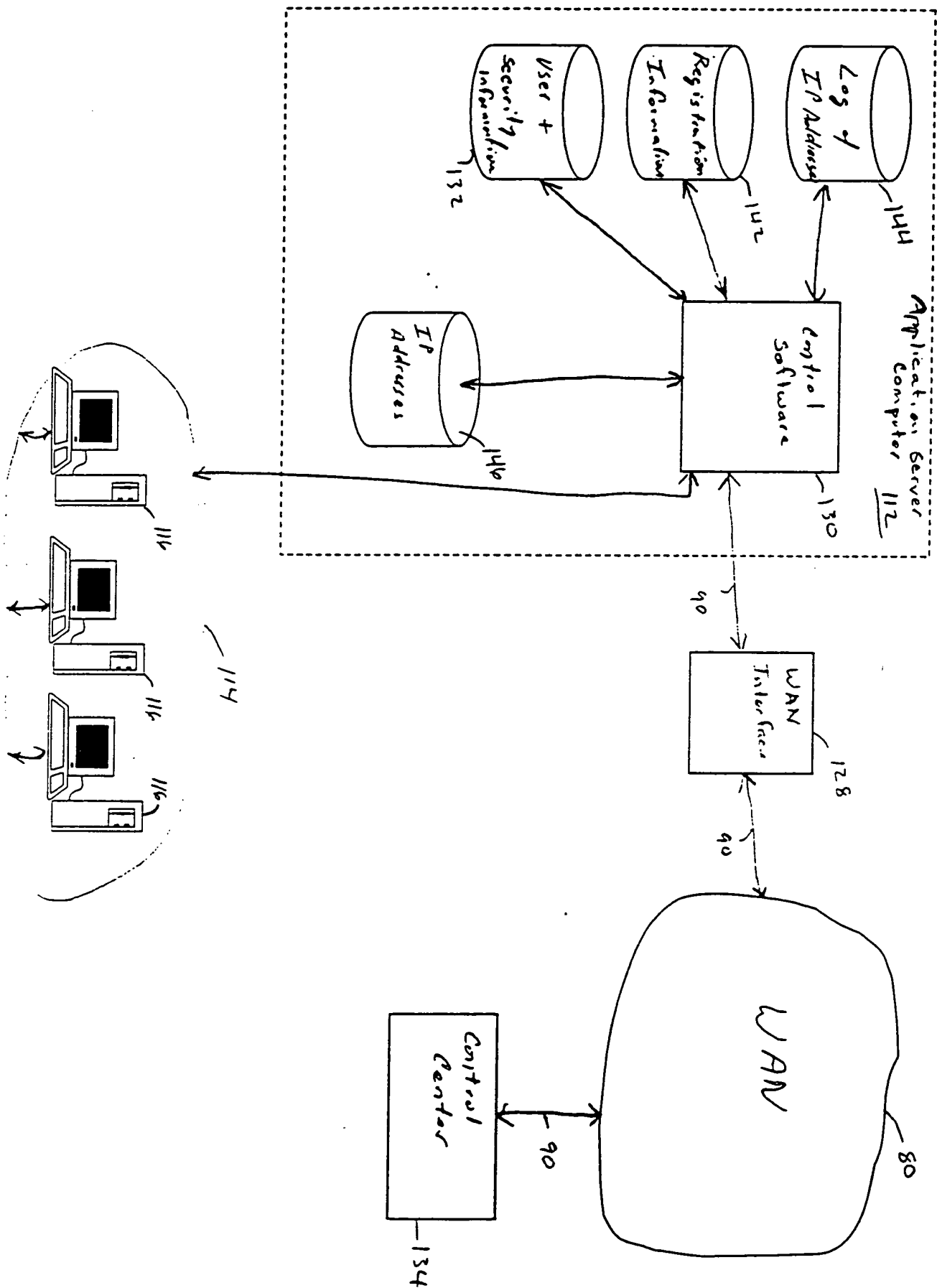
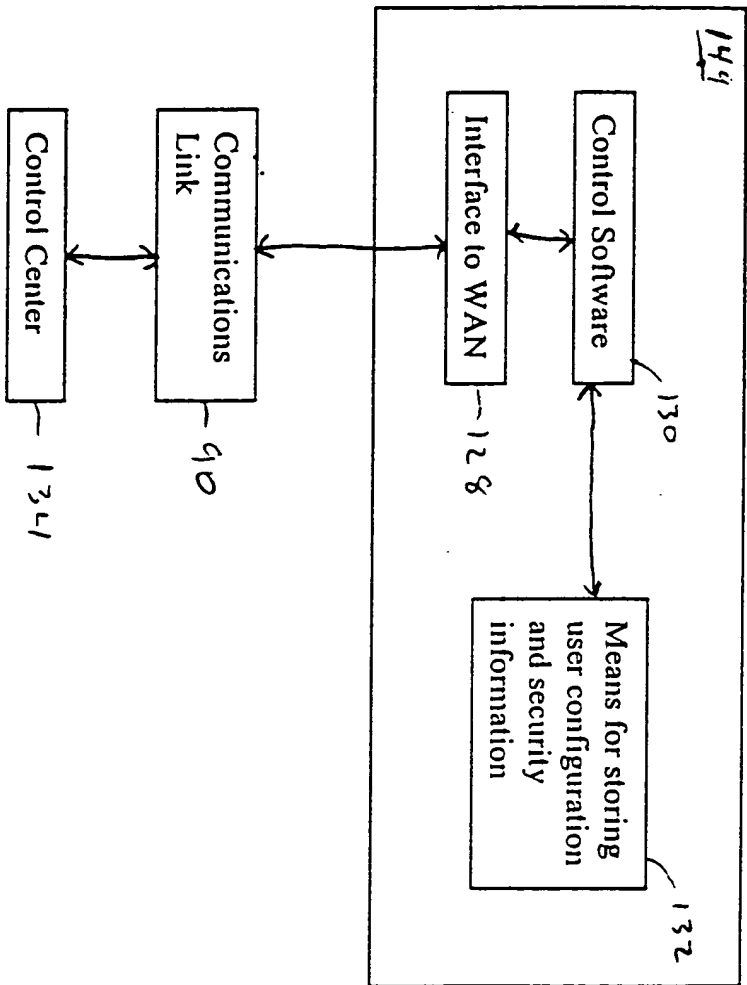
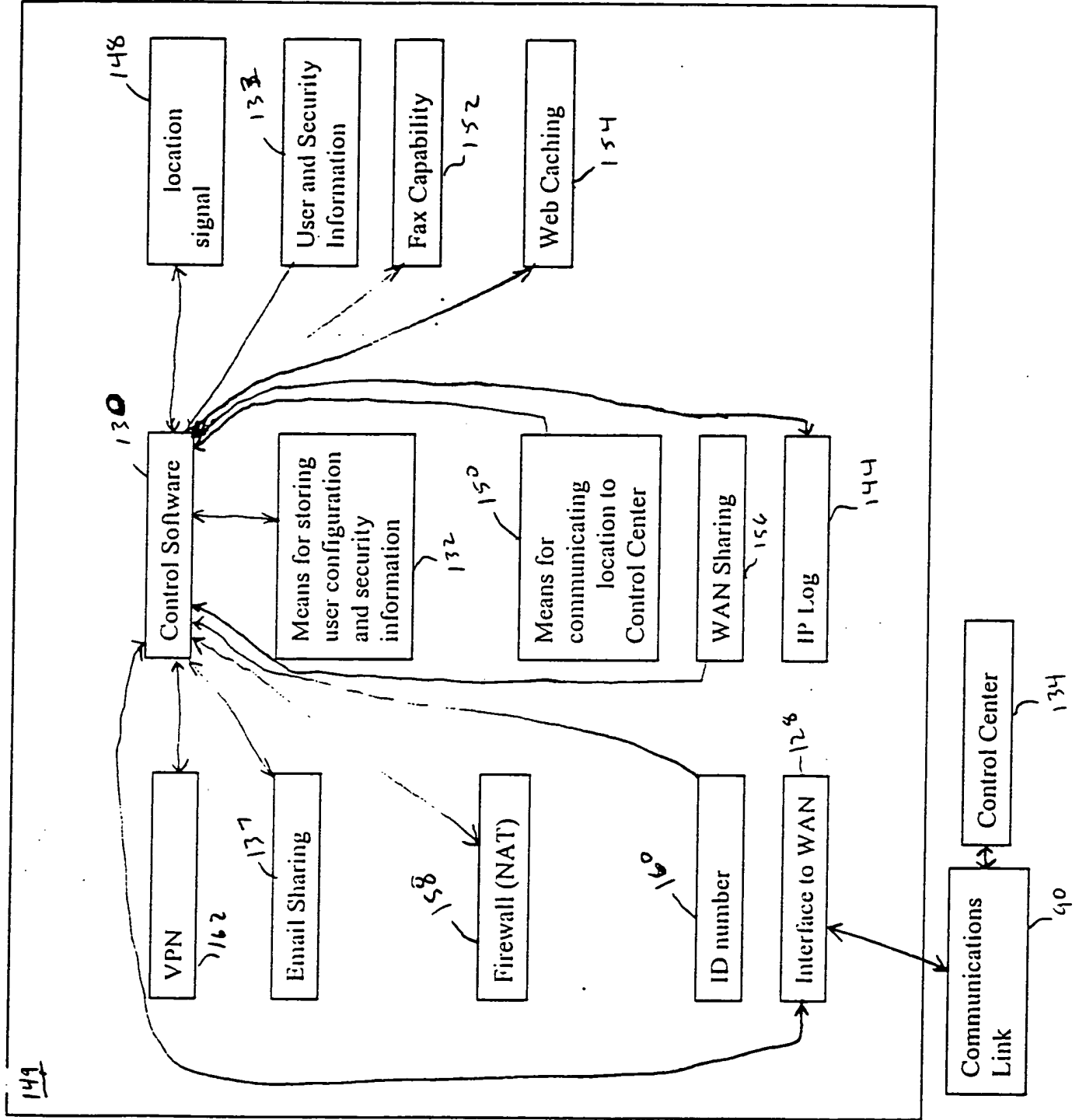


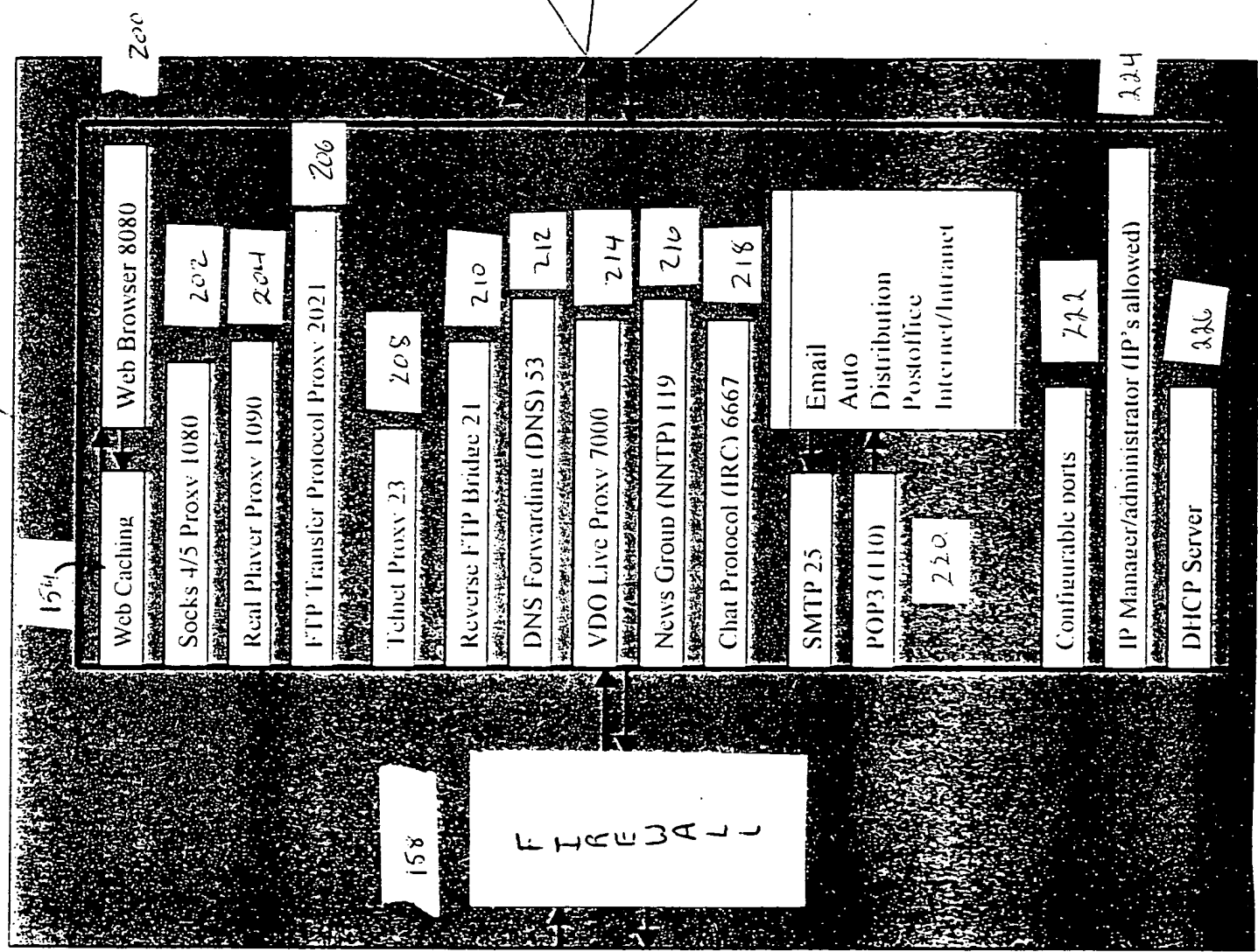
Fig 11.





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